

**AMENDMENTS TO THE CLAIMS**

Please amend claims as shown below. Claims 1-8 are amended for non-statutory reasons, to better place them in standard U.S. patent practice format. Please add new claim 9, as shown below.

This listing of claims 1-9 will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A data-processing system, comprising:  
a microprocessor [PRC], and;  
a communication device [COM] communicating with an electronic module [MOD] intended to send a convention signal to ~~the~~ said microprocessor, and  
~~characterized in that the data-processing system comprises~~ a hardware circuit [HARD] allowing an inversion ~~or no inversion of the~~ ~~an~~ order of bits of a word as a function of ~~the~~ ~~a~~ value of ~~said~~ ~~the~~ convention signal during a transfer of ~~said~~ ~~the~~ word between ~~the~~ ~~said~~ electronic module [MOD] and ~~the~~ ~~said~~ microprocessor [PRC].
2. (Currently Amended) A The data-processing system as claimed in claim 1, ~~characterized in that~~ wherein said electronic module [MOD] is a Subscriber Identity Module card of the SIM type.
3. (Currently Amended) A The data-processing system as claimed in claim 1, ~~characterized in that~~ wherein said hardware circuit [HARD] allows inversion ~~or no inversion~~ of the value of the bits of ~~said~~ the word as a function of the value of ~~said~~ the convention signal.

4. (Currently Amended) A The data-processing system as claimed in claim 1, characterized in that wherein said hardware circuit [HARD] comprises includes switches a switch [SWHMP, SWHPM] and [SWHPM], a right shift registers [RXMP] and register [RXMP, RYPM] electrically connected to said switch, and a left shift registers [RYMP] and register [RYMP, RXPM] electrically connected to said switch.

5. (Currently Amended) A terminal, comprising:  
a microprocessor [PRC], and;  
a communication device [COM] communicating with an electronic module [MOD] intended to send a convention signal to the said microprocessor; and characterized in that the terminal comprises a hardware circuit [HARD] allowing an inversion or no inversion of the an order of bits of a word as a function of the a value of said the convention signal during a transfer of said the word between the said electronic module [MOD] and the said microprocessor [PRC].

6. (Currently Amended) A The terminal as claimed in claim 5, characterized in that wherein said electronic module [MOD] is a Subscriber Identity Module card of the SIM type.

7. (Currently Amended) A The terminal as claimed in claim 5, characterized in that wherein said hardware circuit [HARD] allows inversion or no inversion of the value of the bits of said the word as a function of the value of said the convention signal.

8. (Currently Amended) A The terminals as claimed in claim 5, characterized in that wherein said hardware circuit [HARD] comprises includes switches a switch [SWHMP, SWHPM] and [SWHPM], a right shift registers [RXMP] and register [RXMP, RYPM] electrically connected to said switch, and a left shift registers [RYMP] and register [RYMP, RXPM] electrically connected to said switch.

9. (New) A data-processing system, comprising:  
a hardware circuit [HARD];  
a communication device [COM] for communicating a contention signal and a word to said hardware circuit [HARD] from one of a microprocessor [PRC] and an electronic module [MOD]; and  
wherein said hardware circuit includes means for implementing one of a direct convention and an indirect convention of an order of bits of the word as a function of a value of the convention signal.